

LOGIC ANALYZER

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ABSTRACT

Due to the fast development of electronic devices, the digital circuit designing takes up more and more percentage in total electronic developments thus in the process of debugging and doing validation in a digital system, one of the common task a designer need to do is the acquisition of digital waveforms. The waveforms can be captured by the device Logic Analyzer.

As digital circuit is too fast to be observed by a human being, the basic idea to capture waveforms at higher speed is to implement the design using ARM controller which internally uses RISC Machine unlike simple processors. ARM based embedded systems, providing a low-cost solution to meet the request of flexibility and testability

Logic analyzer is a dedicated application The main objective is to use module as powerful Lab equipment to check and verify the design under test (digital circuit) for design and verification engineers with smaller size and less expensive.